

FIG. 1

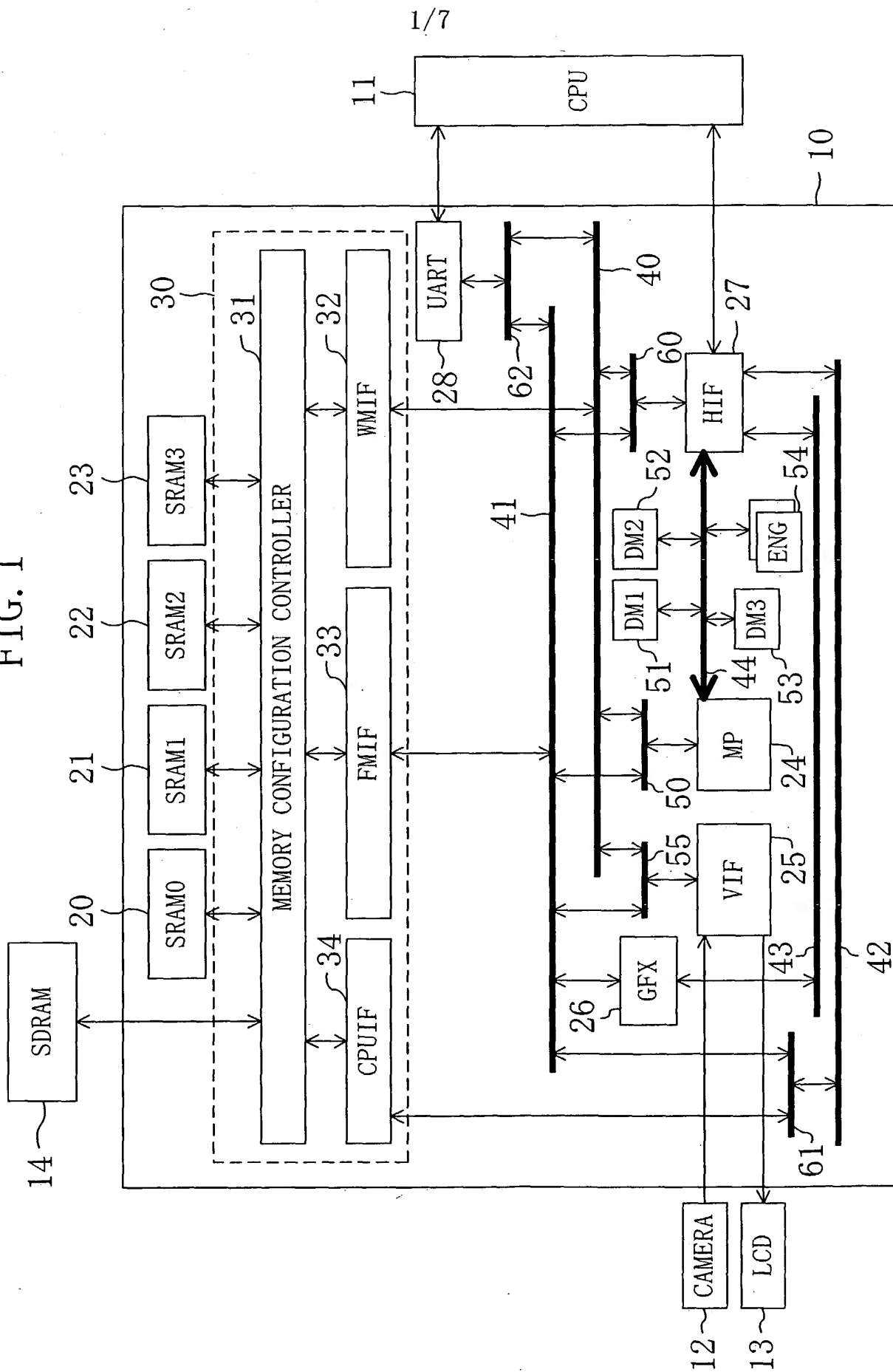


FIG. 2

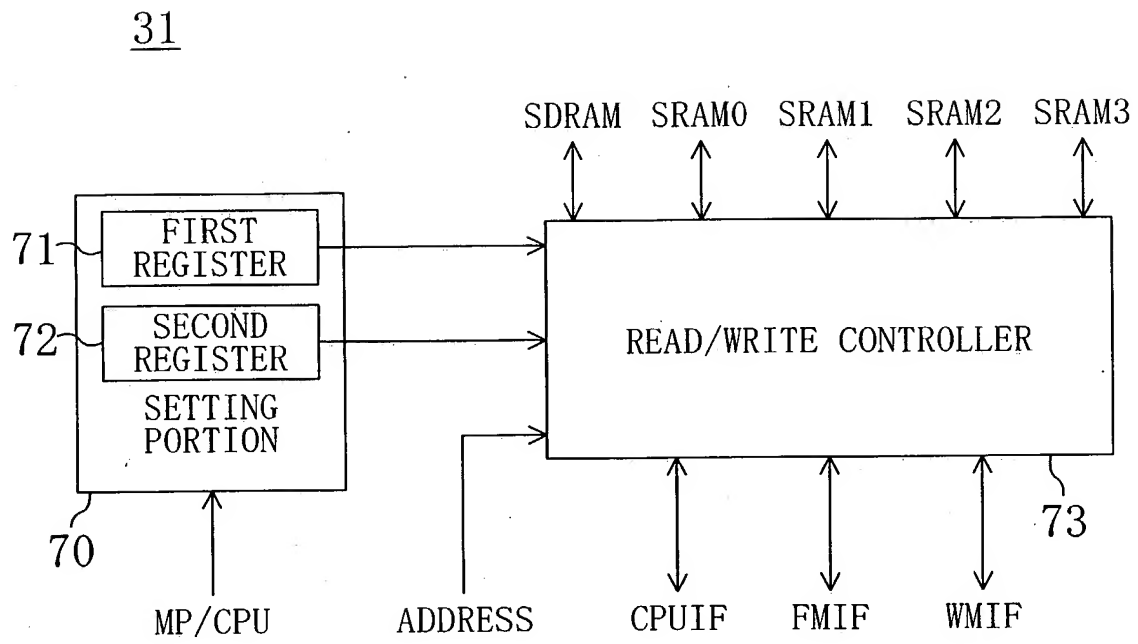


FIG. 3

operating mode	MPEG-4 processing (MP)	video display (VIF, GFX) processing	CPU processing
A	8Mbit		
B	6Mbit	2Mbit	
C	4Mbit	4Mbit	
D	4Mbit	2Mbit	2Mbit
E		4Mbit	4Mbit
F		8Mbit	
G			8Mbit

FIG. 4A

memory	SDRAM		SRAM3		SRAM2		SRAM1		SRAM0	
first register	bit9	bit8	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
initial value	00		00		00		00		00	

FIG. 4B

bit1	bit0	meaning
0	0	work region
0	1	frame region
1	0	CPU region
1	1	unused

FIG. 5A

memory	SDRAM	SRAM3	SRAM2	SRAM1	SRAM0
second register	bit5-4	bit3	bit2	bit1	bit0
initial value	00	0	0	0	0

FIG. 5B

bit0	meaning
0	2Mbit
1	4Mbit

FIG. 5C

bit5	bit4	meaning
0	0	unused
0	1	64Mbit
1	0	128Mbit
1	1	256Mbit

FIG. 6B

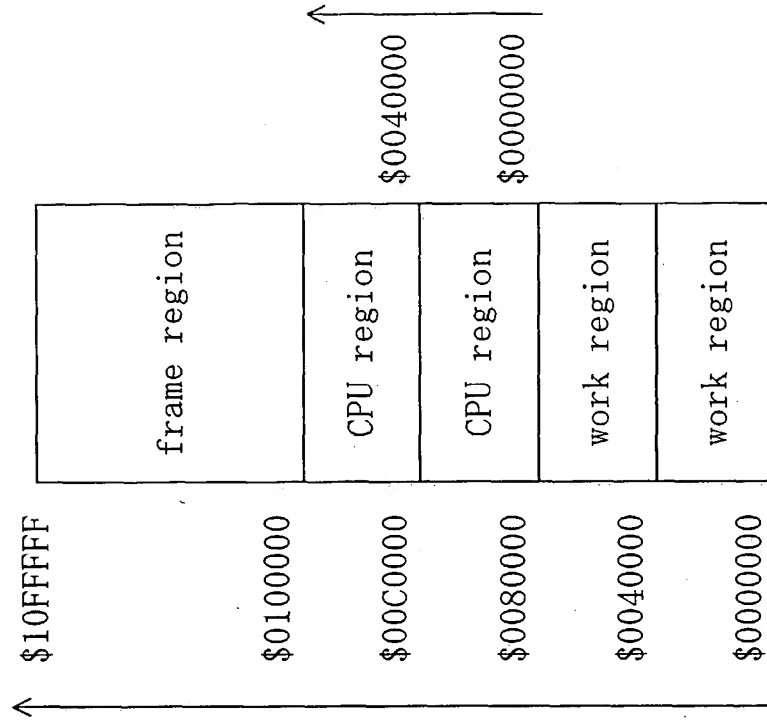


FIG. 6A

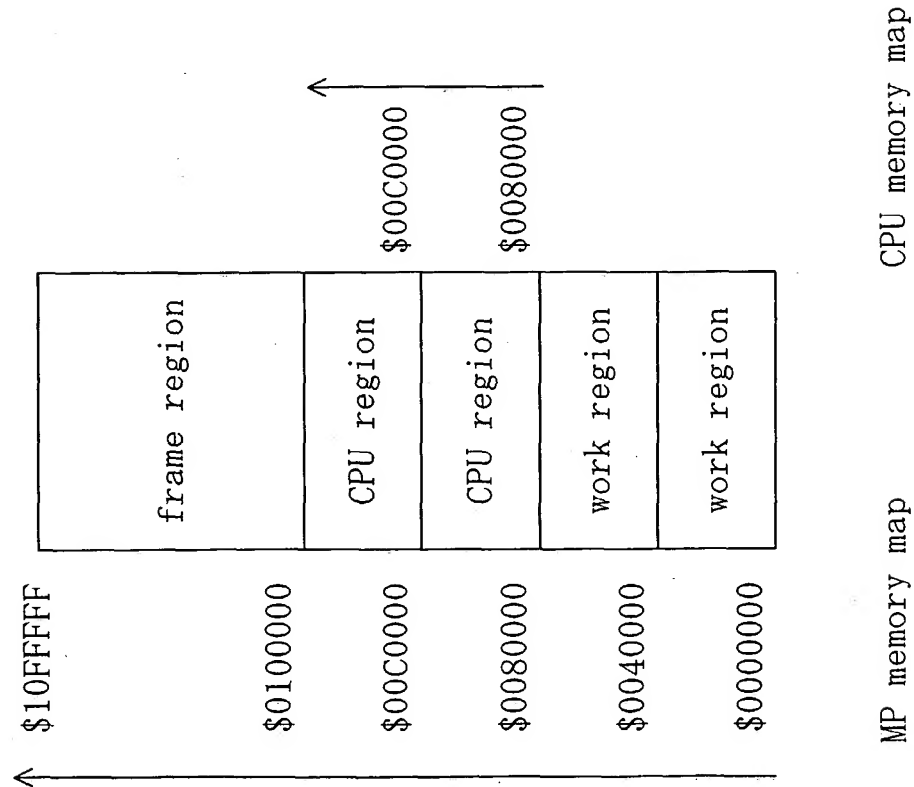


FIG. 7

